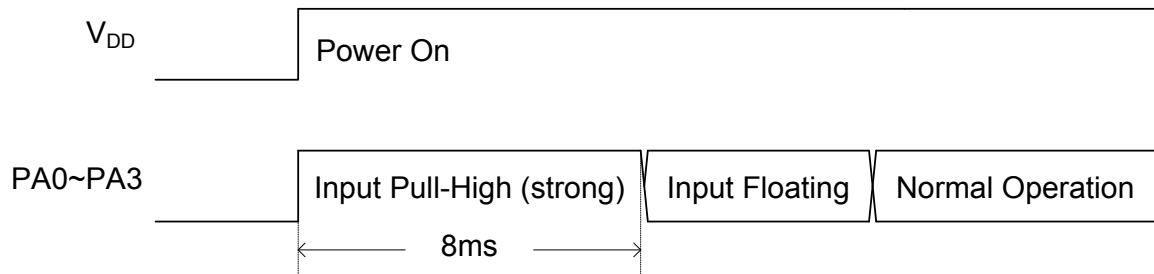# Notices for Powered-on Pins of NY6PxxxJ OTP Series

Description: When NY6P025J/055J/085J/185J/345J is powered on, the PA0 ~ PA3 pins will be kept in the input pull-high(strong) status for about 8ms. Please pay attention to the design plan of PA0 ~ PA3 pins.

Reason: When the input type of PA0 ~ PA3 pin is set as floating, the PA0 ~ PA3 pin will be kept in the input pull-high(strong) for about 8ms, then into the input floating status as shown below.

V_{DD} | Power On

PA0~PA3 | Input Pull-High (strong) | Input Floating | Normal Operation

8ms

Solution:

1. It's recommended to set PA0 ~ PA3 as input to avoid pull-high affects external circuits when power is on.
2. If the PA0 ~ PA3 is set as output pin, please confirm that the pull-high won't make malfunction to external circuit.

   For examples, use sink connection for LED applications, or use PNP transistors for BJT driver motor applications.